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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/452,329	11/30/1999	GARY L. GRAUNKE	42390.P7947	1161
7590 05/24/2004			EXAMINER	
ALOYSIUS T	C AU YEUNG	LAFORGIA, CHRISTIAN A		
BLAKELY SO	KOLOFF TAYLOR & ZA	FMAN LLP		
12400 WILSHIRE			ART UNIT	PAPER NUMBER
7TH FLOOR			2131	\bigcirc
LOS ANGELES	S, CA 90025		DATE MAILED: 05/24/2004	, X

Please find below and/or attached an Office communication concerning this application or proceeding.

X

	Application No.	Applicant(s)				
	09/452,329	GRAUNKE ET AL.				
Office Action Summary	Examiner	Art Unit				
	Christian La Forgia	2131				
The MAILING DATE of this communication Period for Reply		_				
A SHORTENED STATUTORY PERIOD FOR RITHE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 Clafter SIX (6) MONTHS from the mailing date of this communication. If the period for reply specified above is less than thirty (30) days, If NO period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	ON. FR 1.136(a). In no event, however, may a roon. a reply within the statutory minimum of thirt beriod will apply and will expire SIX (6) MON statute, cause the application to become AB	ONTH(S) FROM eply be timely filed y (30) days will be considered timely. THS from the mailing date of this communication. ANDONED (35 U.S.C. § 133).				
Status						
 1) ⊠ Responsive to communication(s) filed on general conditions. 2a) ☐ This action is FINAL. 2b) ☐ Since this application is in condition for all 	This action is non-final.	ers, prosecution as to the merits is				
closed in accordance with the practice und	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) Claim(s) 1-41 is/are pending in the application 4a) Of the above claim(s) 1-21 is/are with the solution of the above claim(s) 1-21 is/are with the solution of the above claim(s) 1-21 is/are with the solution of the so	drawn from consideration. and/or election requirement. miner. are: a) □ accepted or b) ☒ objuiction of the drawing(s) be held in abeyar correction is required if the drawing.	ce. See 37 CFR 1.85(a). (s) is objected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of: 1. Certified copies of the priority docur 2. Certified copies of the priority docur 3. Copies of the certified copies of the application from the International Book * See the attached detailed Office action for a	ments have been received. ments have been received in A priority documents have been ureau (PCT Rule 17.2(a)).	pplication No received in this National Stage				
Attachment(s)	√ □ □ □ □ □ □	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\				
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-94 Information Disclosure Statement(s) (PTO-1449 or PTO/S Paper No(s)/Mail Date 	8) Paper No(s	Summary (PTO-413) s)/Mail Date nformal Patent Application (PTO-152) 				

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DETAILED ACTION

- 1. The amendment filed on 04 March 2004 is noted and made of record.
- 2. Claims 1-41 are presented for examination.
- 3. Claims 1-21 have been cancelled as per Applicant's request.

Drawings

4. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description: 302. A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Allowable Subject Matter

- 5. Claims 21-41 are allowed.
- 6. The following is an examiner's statement of reasons for allowance:

As per claim 22, it is well known in the art for an apparatus to produce a pseudo random sequence, comprising: a data bit generator to produce a principal data stream; multiple data bit generators to create additional data streams; a storage structure responsive to the additional data streams having multiple bit storage locations to store the bits of the principal data stream; and a shuffle unit coupled with the data bit generators to modify the principal data stream by combining the bits of the principal data stream with past bits of the principal data stream stored in the storage structure.

There are no teachings in the prior art of storing the principal data stream in the storage locations in a pseudo random order based on an order of bits in the additional data streams

(Emphasis added). Furthermore there is no teaching of combining the bits of the principal data

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randomly selected from the storage structure based on an order of the bits in the additional data streams to produce a pseudo random sequence (Emphasis added). Since no teachings or motivation can be found of the abovementioned limitations, claim 22 is therefore novel and non-obvious.

As per claim 29, it is well known for a method to generate a data stream, comprising: generating a first and a second bit sequence; storing bits from the first sequence in a memory structure; retrieving stored bits of the first sequence from the memory structure; bit-wise modifying the bits of the first sequence with the stochastically retrieved bits to produce a pseudo random data stream.

There is no teaching in the prior art of retrieving stored bits of the first sequence from the memory structure in a stochastic order, the order based at least in part on a bit order of the second sequence (Emphasis added). Since no teachings or motivation can be found of the abovementioned limitation, claim 29 is therefore novel and non-obvious.

As per claim 35, its is well known in the art for a stream cipher generator comprising: a first data bit generator to produce a first stream of data bits; a memory having a read and write port to receive and store bits from the first stream of data bits; a second data bit generator to produce a second stream of data bits; a read and write port controller coupled to the memory; and a combiner to receive the first stream of data bits and the bits read from the memory, and modify

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the first stream of data bits with the bits read from the memory to produce a pseudo random sequence.

There is no teaching in the prior art of a read and write port controller coupled to the memory and responsive to the second stream of data bits, to control the read and write functions of the memory based, at least in part, on the sequence of bits in the second stream of data bits (Emphasis added). Since no teachings or motivation can be found of the abovementioned limitation, claim 29 is therefore novel and non-obvious.

7. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

- 8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- 9. The following patents are cited to further show the state of the art with respect to stream ciphers with a combiner function, such as:

United States Patent No. 4,082,217 to Michener, which is cited to show a method and apparatus for securing access to a computer facility.

United States Patent No. 5,323,338 to Hawthorne, which is cited to show pseudo-random sequence generators.

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United States Patent No. 5,566,099 to Shimada, which is cited to show a pseudo-random number generator.

United States Patent No. 5,751,808 to Anshel et al., which is cited to show multi-purpose high speed cryptographically secure sequence generators based on zeta-one-way functions.

United States Patent No. 5,341,425 to Wasilewski et al., which is cited to show a method and apparatus for uniquely encrypting data at a plurality of data transmission sites for transmission to a reception site.

United States Patent No. 5,577,124 to Anshel et al., which is cited to show multi-purpose high speed cryptographically secure sequence generators based on zeta-one-way functions.

United States Patent No. 6,192,385 to Shimada, which is cited to show a pseudo-random number generator.

United States Patent No. 5,598,154 to Wilson et al., which is cited to show generating and utilizing pseudo-noise code sequences.

- This application is in condition for allowance except for the following formal matters:The Objection to the Drawings.
- 11. Prosecution on the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.
- 12. A shortened statutory period for reply to this action is set to expire **TWO MONTHS** from the mailing date of this letter.
- 13. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christian La Forgia whose telephone number is (703) 305-7704. The examiner can normally be reached on Monday thru Thursday 7-5.

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14. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz Sheikh can be reached on (703) 305-9648. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

15. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Christian LaForgia Patent Examiner Art Unit 2131

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